# DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING NATIONAL INSTITUTE OF TECHNOLOGY, TIRUCHIRAPPALLI

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Name of the	COURSE PLA	N – PART I		
programme and specialization	B.Tech. / Electrical and Electronics Engineering			
Course Title	DC MACHINES AND TRANSFORMERS LABORATORY			
Course Code	EELR11	No. of Credits	2	
Course Code of Pre- requisite subject(s)	NIL	the A very		
Session	July 2018	Section (if, applicable)	В	
Name of Faculty	(if, applicable)		EEE	
Email			0431-250 3255	
Name of Course Coordinator(s) (if, applicable)	NA	8 V	V <sub>2</sub> B	
E-mail	NA	Telephone No.	NA	
Course Type	x Core course	Elective cou	irse	
Syllabus (approved in	BoS)	3.		
	List of Experim	ients		
A demonstration of the stop to be done in an introduce	static and rotational electrical ctory class.	machines (constructio	nal details) is ought	
Open circuit and lo	ad characteristics of DC shur	nt/compound generato	r	
Swinburne's test are	nd Speed control of DC shun	t motor		
<ul> <li>Load test on DC sh</li> </ul>	nunt motor			
<ul> <li>Load test on DC se</li> </ul>	eries motor			
Open circuit and sh	nort circuit test on single-phas	se transformer		
<ul> <li>Sumpner's test</li> </ul>		*		
	of single-phase transformer			
Electrical braking in			2	
Three-phase transfer	ormer connections			
Mini-Project				

#### **COURSE OBJECTIVES**

To expose the students to the basic concepts of various types of electrical machines such as DC Machines and Transformers and their applications. The students will be exposed to the relevant performance characteristics and their control techniques of such electrical machines.

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Course Outcomes	Aligned Programme Outcomes (PO)			
Upon completion of the course the students would be able to				
1. Interpret the constructional details of the DC machines and Transformers and also understand the significance of different connections of three-phase transformers.				
<ol><li>Estimate or test the performance of any DC machine (shunt, series or compound) and single-phase transformer, by conducting suitable experiments and report the results.</li></ol>	2, 7, 8, 9, 10, 12			
3. Experiment and analyze the various speed control and braking techniques for DC motors.	2, 7, 8, 9, 10, 12			
<ol> <li>Develop simulation models and prototype modules in view of implementing any control technique upon dc motors and single- phase transformers for various applications.</li> </ol>	2, 7, 5, 8, 9, 10, 12			

### COURSE PLAN - PART II

#### **COURSE OVERVIEW**

An Electrical Machine is an electro-mechanical energy converter. It is a device that converts either mechanical energy to electrical energy - generator or electrical energy to mechanical energy - motor. An electrical machine is a dual machine and hence each machine can be used as either generator or a motor. Almost all practical electrical machines convert energy from one form to another through the action of a magnetic field. Electrical machines provide green energy production and hence insight into such machines, their design and working principles become inevitable for an electrical engineer. Only machines using magnetic fields as medium of energy conversions are considered in this course.

The *transfomer* is an electrical device that transfoms AC electrical energy at one voltage level to another voltage level, operated at same frequency. Since transformers also operate on the principle of electromagnetic induction, similar to generators and motors, depending on the action of magnetic filed to accomplish the change in voltage level, they are usually studied together with electrical machines. Morevover transformers play a major role in power transfer in large power systems. Hence transformers are also taught along with the electrical machines. These three types of electric devices are ubiquitous in modern daily life.

Electric motors are used widely in the home run refrigerators, freezers, vacuum cleaners, blenders, air conditioners, fans and many similar appliances. In the workplace, motors provide the motive power for all tools. Of course, generators are necessary to supply the power used by all these motors. Hence as an Electrical Engineer, it is essential to know the operating characteristics and relevant performace parameters of above said electrical machines.

In this laboratory course, the performance of various DC machines and Transformers is tested by conducting standard experiments. In this lab students will get complete exposure to power level circuit connections, various types of analog measuring instruments.

S.No.	Week/Contact Hours	List of experiments/ Activities	Mode of Delivery	
1.	2 <sup>nd</sup> Week of July (9 <sup>th</sup> to 13 <sup>th</sup> July)	Course plan details General introduction to laboratory at class room	Lecture PPT and C&T	
2.	3 <sup>rd</sup> Week of July (16 <sup>th</sup> to 20 <sup>th</sup> July)	Open circuit characteristics of DC shunt generator	Experimentation	
3.	4 <sup>th</sup> Week of July (23 <sup>rd</sup> to 27 <sup>th</sup> July)	Load characteristics of DC Shunt generator	Experimentation	
4.	1 <sup>st</sup> Week of August (30 <sup>th</sup> July to 3 <sup>rd</sup> August)	Load Characteristics of DC Compound generators	Experimentation	
5.	2 <sup>nd</sup> Week of August (6 <sup>th</sup> to 10 <sup>th</sup> August)	Speed control of DC shunt motor	Experimentation	
6.	3 <sup>rd</sup> Week of August (20 <sup>th</sup> to 24 <sup>th</sup> August)	Load Test on DC shunt motor	Experimentation	
7.	4 <sup>th</sup> Week of August (13 <sup>th</sup> to 17 <sup>th</sup> August)	Assessment 1		
8.	5 <sup>th</sup> Week of August (27 <sup>th</sup> to 31 <sup>st</sup> August)	Load test DC series motor	Experimentation	
9.	1 <sup>st</sup> Week of Sep. (3 <sup>rd</sup> to 7 <sup>th</sup> Sep)	Open circuit and Short circuit test on Single-phase transformer	Experimentation	
10.	2 <sup>nd</sup> Week of Sep. (10 <sup>th</sup> to 14 <sup>th</sup> Sep.)	Swinburne's test on DC shunt machine	Experimentation	
11.	3 <sup>rd</sup> Week of Sep. (17 <sup>th</sup> to 21 <sup>st</sup> Sep.)	Load test on single phase transformer	Experimentation	
12.	4 <sup>th</sup> Week of Sep. (24 <sup>th</sup> to 28 <sup>th</sup> Sep.)	Assessment 2		

13.	1 <sup>st</sup> Week of October (1 <sup>st</sup> to 5 <sup>th</sup> Oct.)	Parallel operation of transformers and Sumpners's test	Demonstration
14.	3 <sup>rd</sup> Week of October (15 <sup>th</sup> to 19 <sup>th</sup> Oct)	Comprehensive performance assessment	written & Viva voce
15.	4 <sup>th</sup> Week of October (22 <sup>nd</sup> to 26 <sup>th</sup> Oct)	Compensation Laboratory & Submission of Lab reports	
16.	5 <sup>th</sup> Week of October (29 <sup>th</sup> Oct. to 2 <sup>nd</sup> Nov.)	Final Assessment (End Semester Examination)	Practical Examination

# COURSE ASSESSMENT METHODS (shall range from 4 to 6)

S.No.	Mode of Assessment	Week/Date	Duration	% Weightage
1	Assesment 1 (Evaluation of experimentation, fair record and Viva voce on 1-5 lab sessions)	During regular lab session for 1-5 experiments	5 sessions	20
2	Assesment 2 (Evaluation of experimentation, fair record and Viva voce on 6-9 lab sessions)	During regular lab session for 6-9 experiments	4 sessions	20
3	Comprehensive performance assessment (written and viva voce)	Week 14 (1 <sup>st</sup> to 5 <sup>th</sup> Oct.)	1 session	10
4	End semester examination	Week 16 (29 <sup>th</sup> Oct. to 2 <sup>nd</sup> Nov.)	3 hours	50

# COURSE EXIT SURVEY (mention the ways in which the feedback about the course shall be assessed)

- Feedback from the students during class committee meetings
- Anonymous feedback through questionnaire (Mid of the semester & End of the semester)
- End semester feedback on Course Outcomes

#### COURSE POLICY

# MODE OF CORRESPONDENCE

1. All the students are advised to check their NITT WEBMAIL regularly. All the correspondence (schedule of classes/ schedule of assessment/ course material/ any other information regarding this course) will be done through their webmail only.

# COMPENSATION ASSESSMENT POLICY

Compensation Lab session will be given for the students who are absent for the regular

session with prior permission.

# **ATTENDANCE POLICY** (A uniform attendance policy as specified below shall be followed)

- > At least 75% attendance in each course is mandatory.
- A maximum of 10% shall be allowed under On Duty (OD) category.
- > Students with less than 65% of attendance shall be prevented from writing the final assessment and shall be awarded 'V' grade.

# ACADEMIC DISHONESTY & PLAGIARISM

- Possessing a mobile phone, carrying bits of paper, talking to other students, copying from others during an assessment will be treated as punishable dishonesty.
- Zero mark to be awarded for the offenders. For copying from another student, both students get the same penalty of zero mark.
- The departmental disciplinary committee including the course faculty member, PAC chairperson and the HoD, as members shall verify the facts of the malpractice and award the punishment if the student is found guilty. The report shall be submitted to the Academic office.

The above policy against academic dishonesty shall be applicable for all the programmes.

#### ADDITIONAL INFORMATION

The faculty member is available for consultation during working hours of the institute.

FOR APPROVAL

Course Faculty

CC-Chairperson

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