

DEPARTMENT OF ELECTRICAL AND ELECTRONICS Engineering
NATIONAL INSTITUTE OF TECHNOLOGY, TIRUCHIRAPPALLI

COURSE PLAN – PART I			
Name of the programme and specialization	B. Tech (for first year Department of Electrical and Electronics Engineering)		
Course Title	INTRODUCTION TO ELECTRICAL AND ELECTRONICS ENGINEERING		
Course Code	EEIR15	No. of Credits	02
Course Code of Pre-requisite subject(s)	—		
Session	July 2018	Section (if, applicable)	A & B
Name of Faculty	Dr. SUNDARESWARAN K	Department	EEE
Email	kse@nitt.edu	Telephone No.	0431-2503255
Name of Course Coordinator(s) (if, applicable)			
E-mail		Telephone No.	
Course Type	<input checked="" type="checkbox"/> Core course <input type="checkbox"/> Elective course		
Syllabus (approved in BoS)			
History, major inventions, scope, significance and job opportunities in electrical and electronics engineering, brief overview of various energy resources. Basics of energy conversion. Power apparatus used in power generation, transmission and distribution. Power apparatus used in various industries. Basic ideas about utility supply, electrical tariff, energy audit and importance of energy saving. Introduction to different types of electrical circuits, house wiring, electronic circuits for signal processing, specifications of electronic components. Brief overview of curriculum, laboratories and various software packages, electronic testing and measuring equipment.			
Reference books:			
1. Clayton Paul, Syed A Nasar and Louis Unnewehr, 'Introduction to Electrical Engineering', 2nd Edition, McGraw-Hill, 1992. 2. Kothari D.P. & Nagrath I.J., 'Basic Electrical Engineering', 2nd Edition, Tata McGraw-Hill, 2001. 3. P.S. Dhogal, 'Basic Electrical Engineering – Vol. I & II', 42nd Reprint, McGraw-Hill, 2012.			
COURSE OBJECTIVES			
This course facilitates the students to get a comprehensive exposure to electrical and electronics engineering.			
COURSE OUTCOMES (CO)			
Course Outcomes	Aligned Programme Outcomes (PO)		
The students shall develop an insightful knowledge on various fundamental elements of electrical and electronics engineering.	1,2,3,4, 5, 9, 10, 12, 13, 14		

- course) will be done through email only.
2. Queries if any can be emailed to the course teacher kse@nitt.edu

COMPENSATION ASSESSMENT POLICY

1. If any student is not able to attend Assessment-1 / Assessment-2 (due to genuine reason, student is permitted to attend the compensation Assessment with 20% weightage (20 marks).
2. In any case, compensation test will not be considered as an improvement test.

ATTENDANCE POLICY (A uniform attendance policy as specified below shall be followed)

- At least 75% attendance in each course is mandatory.
- A maximum of 10% shall be allowed under On Duty (OD) category.
- Students with less than 65% of attendance shall be prevented from writing the final assessment and shall be awarded 'V' grade.

ACADEMIC DISHONESTY & PLAGIARISM

- Possessing a mobile phone, carrying bits of paper, talking to other students, copying from others during an assessment will be treated as punishable dishonesty.
- Zero mark to be awarded for the offenders. For copying from another student, both students get the same penalty of zero mark.
- The departmental disciplinary committee including the course faculty member, PAC chairperson and the HoD, as members shall verify the facts of the malpractice and award the punishment if the student is found guilty. The report shall be submitted to the Academic office.

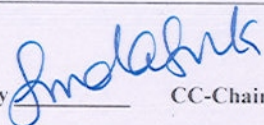
The above policy against academic dishonesty shall be applicable for all the programmes.

ADDITIONAL INFORMATION

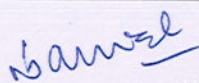
- Be aware of B.Tech. regulations in the Institute website for passing minimum, redo, formative assessment, grades, credits etc.

FOR APPROVAL

Course Faculty



CC-Chairperson



HOD

