DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING NATIONAL INSTITUTE OF TECHNOLOGY, TIRUCHIRAPPALLI

No. of Credits

04

NETWORKS AND LINEAR SYSTEMS

COURSE OUTLINE TEMPLATE

EEPC19

Course Title

Course Code

De	epartment	EEE		Facul	ty			S.Mu	kesh	Mut	hu
Pr	Pre-requisites Course Code				MAIR32 & EEPC11						
100000	ourse								7		
	oordinator(s)										
	ther Course Teacl itor(s) E-mail	her(s) /		Tel/M	ob No.			9500	4045	15	
Co	ourse Type	√ Cor	e course		Elect	ive c	ourse				
											- 7
C	DURSE OVERVIE	W									
ele an tim air an	etworks, signals are ectrical engineering d circuits is called ne/frequency doma m of this course is d continuous-time plication of these of	g equipmented as systematic character to provide signals, Fo	t/product v em. Having ization is a students w ourier serie	which h g a go an abso ith an u es, Fou	andles od und lute mu ndersta rier tra	signa dersta ust for andin nsfor	als us anding rany e g of th ms, La	of selectricate	ectric ignals cal en cepts	al ness and a signed	etworks d their er. The liscrete
CC	OURSE OBJECTIV	/ES	1								
ap no	emphasize the reproach for (i) and n-periodic source, nplifications, (iv) tra	alyzing the ((ii) one-port	electric cir and two- p	cuit / s	ystem works,	excite (iii) sv	ed with	non- model	sinus ing a	oidal nd	olving and
CC	OURSE OUTCOM	ES (COs)			Alian	ed Pr	ogran	nma (Jutoc	mac	(POc)
Upon completion of the course, the students will			Aligned Programme Outcomes (POs COs / POs Course outcomes(COs)					(FOS)			
100	able to		no otagoni				1	2	3	4	
17/10/2004						1	Н	Н	Н	Н	
1.	Understand the s				100	2	Н	Н	Н	Н	
	and Fourier Trans		oply them f	or	Ö	3	NA	NA	NA	Н	
	typical electrical s	systems.			s (F	4	NA	NA	NA	NA	
2.	Apply Laplace Tra	ansform for t	he solution	n of	me	5	NA M	NA	NA NA	NA NA	
	typical electric cir				00	7	M	H	H	M	
					l o	8	M	M	M	M	
	port network para				Je l	9	L	L	L	L	
3.	Model the system	s in transfer	function a	nd	E	10	М	M	M	L	
	state-space doma	ain and analy	yze the sys	stem	gra	11	М	M	М	M	
	using these mode	Secretary of the Control of the Cont			Programme Outcomes (POs)	12	М	М	М	М	
1	Apply Z-transform		alvoic of di	coroto	-	13	NA	NA	NA	NA	
4.	time system	is for the an	arysis or di	screte		14	NA	NA	NA	NA	

S.No.	Week	Topic	Mode of Delivery
1.	1 st week of January 17 (4 to 6)	Discussion on course plan Introduction to Fourier series - Fourier coefficients	
	(2 Contact Hours)	Soundonio	
2.	2 nd week of January 17 (9 to 13)	Effect of symmetry on the Fourier coefficients – Trigonometric form of Fourier series – Application to simple circuits –	
	(4 Contact Hours)	Average power – power factor and rms value calculations	
3.	3 rd week of January 17 (16 to 20)	Exponential form of the Fourier series – Fourier transform – Fourier transforms for typical signals – application to simple circuits	
	(4 Contact Hours)	Special approach to omplo should	
	20.01.2017 : 50 minutes (10.30 am – 11.20 am)	(Assessment-3 : Solving numerical examples from Unit I – 5 marks)	
4.	4 th week of January 17 (23 to 27)	Review of Laplace transform – Laplace transform for typical signals – representation of signals in terms of elementary signals-	
	(4 Contact Hours)	complex frequency-time response of the circuits	Lecture /
5.	Last week of January & 1st week of February 17 (30-31 Jan and 1 to 3 Feb)	circuit elements in s-domain-circuit analysis in s-domain-transfer function-poles and zeros-application of initial and final value theorems-frequency response from pole-zero plot-driving point impedances	Tutorial C & T / PPT c any suitable mode
0	(4 Contact Hours)		
6.	2 nd week of February 17 (6 to 10) (4 Contact Hours)	Two-port networks	
	10.02.2017 : 50 minutes (10.30 am – 11.20 am)	(Assessment-3 : Solving numerical examples from Unit II – 5 marks)	
7.	3 rd week of February 17 (13 to 17)	Open-loop system-closed loop system - Transfer function modeling for the simple electrical systems-Differential equation of	
	(3 Contact Hours)	translational and rotational mechanical systems	
		(Assessment - 1) Written test	
8.	4 th week of February 17 (20 to 24)	force-voltage and force-current analogous systems-transfer function modeling of	
	(4 Contact Hours)	mechanical systems - Block diagram representation-Block diagram reduction	

		technique
9.	Last week of Feb. 17 to 1st week of March 17 (27 Feb to 3 March)	signal flow graph-Mason's gain formula
	(3 Contact Hours) 01.03.2017 : 50 minutes (11.20 am – 12.10 am)	(Assessment-3 : Solving numerical examples from Unit III – 5 marks)
0.	2 nd week of March 17 (6 to 10) (4 Contact Hours)	Concepts of state and state variables-state space modeling for simple electrical and mechanical systems
1.	3 rd week of March 17 (13 to 17)	State transition matrix – solution of state equations - Solution of state equations-numerical examples
	17.03.2017 : 50 minutes (10.30 am – 11.20 am)	(Assessment-3 : Solving numerical examples from Unit IV – 5 marks)
2.	4 th week of March 17 (20 to 24) (3 Contact Hours)	Introduction to discrete time systems – properties – z-transform for typical signals-region of convergence
		(Assessment - 2) Written test
3.	5 th week of March 17 (27 to 31) (4 Contact Hours)	Inverse z-transform – simple examples - Difference equation – solution of difference equations – stability analysis
4.	1 st week of April 17 (3 to 7)	Numerical examples from unit V.
	(4 Contact Hours)	Review of all units
5.	2 nd week of April 17 (10 to 14)	Compensation Assessment (Written test)
6.	3 rd / 4 th week of April 17 Date of examination will	ASSESSMENT – 4
	be intimated later	(Written test)

C & T : Chalk and Talk and PPT : Power Point

COURSE ASSESSMENT METHODS

S.No.	Mode of Assessment	Week/Date	Duration	% Weightage
1	Assessment-1 (First 2 Units) (Written test)	3 rd week of February 17 (13 to 17)	60 Minutes	20

2	Assessment-2 (3 rd and 4 th Units) (Written test)	4 th week of March 17 (20 to 24)	60 Minutes	20
3	Assessment-3 Assignment / Open book test / Quiz (4 Nos. each for 5 marks)	During the regular details will be		20
CPA	Compensation Assessment (First 4 Units) (Written test)	2 nd week of April 17 (10 to 14)	60 Minutes	20
5	Assessment-4 (All units) (Written test)	3 rd / 4 th week of April 17	120 Minutes	40

Note:

- 1. Exact date and time for the assessments (1,2 & 4) will be informed later.
- 2. Attending all the assessments (i.e., Assessment 1 to 4) are MANDATORY for every student.
- If any student is not able to attend Assessment-1 / Assessment-2 due to genuine reason, he/she is permitted to attend the Compensation Assessment (CPA) with 20 % weightage (20 marks).
- 4. At any case, CPA will not be considered as an improvement test.

Grading the students

- Grading will be based on the clusters (range) of the total marks (all the assessments i.e., Assessment 1 to 4, put together for each student) scored. For grading, Gap theory or Normalized curve method will be used to decide the clusters (range) of the total marks.
- The passing minimum shall be class mean by two or maximum by three, whichever is lower. Hence, every student is expected to score the minimum mark to pass the course. Otherwise the student would be declared fail and 'F' grade will be awarded.

ESSENTIAL READINGS : Textbooks, reference books Website addresses, journals, etc

Text Books:

- 1. D. Roy Choudhury, 'Networks and Systems', New Age International Publications, 1st Edition, 2013.
- James W. Nilsson and Susan A. Riedel, 'Electric Circuits', Pearson Education Publications, 9th Edition, 2011.
- F.F.Kuo, 'Network Analysis and Synthesis', John Wiley Inc Publications, 2nd Edition, 2010.
- M.E. Van Valkenburg, 'Network Analysis', PHI Learning Publications, 3rd Edition, 2014.

Reference Books:

- 1. Cheng.D. K, 'Analysis of Linear System', Addison Wesley Publications, Revised Edition, 2009.
- 2. William D. Stanley, 'Network Analysis with Applications', Pearson Education Publication, 2009.
- Hayt, W. H., Kemmerly J. E. & Durbin, 'Engineering Circuit Analysis', McGraw Hill Publications, 8th Edition, 2013.

Following NPTEL course materials will form the additional references:

- Networks and systems by Prof. V.G.K. Murti, IIT Madras. Web-site: http://nptel.ac.in/courses/108106075/#
- Circuit theory by Prof. S.C. Dutta Roy, IIT Delhi. Web-site: http://nptel.ac.in/courses/108102042/#

COURSE EXIT SURVEY (mention the ways in which the feedback about the course is assessed and indicate the attainment also)

Feedback from the students during class committee meetings Anonymous feedback through questionnaire

COURSE POLICY (including plagiarism, academic honesty, attendance, etc.)

CORRESPONDENCE

- All the students are advised to check their NITT WEBMAIL regularly. All the correspondence (schedule of classes/ schedule of assessment/ course material/ any other information regarding this course) will be done through their webmail.
- Queries (if required) may be emailed to me / contact me during 3.00 pm to 4.00 pm on Tuesday and Thursday with prior intimation for any clarifications.

ATTENDANCE

- Attendance will be taken by the faculty in all the contact hours. Every student should maintain minimum 75 % physical attendance in these contact hours to attend the Assessment-4 i.e., last assessment.
- Any student, who fails to maintain 75% attendance, however, having score more than 50 % marks (i.e., more than 30 marks) in first three assessments will be eligible for attending the last assessment (Assessment-4).
- 3. Students having less than 75% attendance at the end of the semester and also having the score less than 60 % marks (i.e., less than 36 marks) in first three assessments will have to REDO the course and hence they are not eligible for attending the last assessment (Assessment-4). 'V' Grade will be awarded for such students.

ACADEMIC HONESTY & PLAGIARISM

- All the students are expected to be genuine during the course work. Taking of information by means
 of copying simulations, assignments, looking or attempting to look at another student's paper or
 bringing and using study material in any form for copying during any assessments is considered
 dishonest.
- Tendering of information such as giving one's program, simulation work, assignments to another student to use or copy is also considered dishonest.
- Preventing or hampering other students from pursuing their academic activities is also considered as academic dishonesty.
- 4. Any evidence of such academic dishonesty will result in the loss of marks on that assessment. Additionally, the names of those students so penalized will be reported to the class committee chairperson and HoD for necessary action.
- Students who honestly producing ORIGINAL and OUTSTANDING WORK will be REWARDED.

ADDITIONAL COURSE INFOR	RMATION
FOR APPROVAL	
S-mukesh.	20 1 H - 0 0 0
Course Faculty 02/01/2017	CC-Chairperson HOD HOD