DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING NATIONAL INSTITUTE OF TECHNOLOGY, TIRUCHIRAPPALLI

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Course Code	EEPC1	1													
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Department	EEE			Faculty			C.Nagamani								
Pre-requisites	1780			-	NA-4							121			
Course Code		Mathematics II (MAIR12)													
Course															
Coordinator(s) (if, applicable)															
Other Course				eleni	hon	o No	_		0404						
Teacher(s)/Tutor(Telephone No.					0431	-250	325	4			
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S.No	. Week		Tani	120,000		The second second		
1	Weeks 1 to 3		Topic			ode of Delivery		
2	(10 contact hours, including two contact	Basics of dc and ac circuits - Concepts				Lecture/ Tutorial		
	hours for problem solving)	numerical examples/ problem solving			Gr	oup work (exercise		
3	Weeks 4 to 6 (10 contact hours,	Source transformation, Network				Lecture / Tutorial		
4	including two contact hours for problem solving)		Theorems, star-delta equivalence numerical examples/ problem solving			oup work (exercise)		
5	Weeks 7 to 9 (10 contact hours.		Resonance, and analysis of			Lecture / Tutorial		
6	including two contact hours for problem solving)		numerical examples/ problem solving			oup work (exercise)		
7	Weeks 10 to 12		Three-phase circuits			_ecture / Tutorial		
8	(10 contact hours, including two contact hours for problem solving)		numerical examples/ problem solving			up work (exercise)		
9	Weeks 13 to 15 (10 contact hours,	Time response of RL, RC and RLC			Lecture / Tutorial			
10	including two contact hours for problem solving)		circuits numerical examples solving	Group work (exercise)				
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S.No.	Mode of Assessmen	t	Week/Date Duration			0/ Mainht		
2	1 st Mid Semester Examination (Written test) (1 st and 2 nd Units)		6 th Week	60 Minutes		% Weightage 20		
	2 nd Mid Semester Examination (Written test) (3rd and 4 th Units)		12 th Week	60 Minutes	5	20		
3	Take Home / Team Task		3 rd to 13 th week	carried ou along with the		10		
4	Retest (Written Test) (1 st to 4 th Unit)		14 th week 60 Minute			20		
5	End Semester Examination (Written test)		16 th week 180 Minutes			5 50		

Note:

- 1. Attending all the assessments (Assessments 1-3 and 5) are MANDATORY for every student.
- 2. If any student is not able to attend Assessment-1 (1st Mid Sem) / Assessment-2 (2nd Mid Sem) due to genuine reason, student is permitted to attend the Assessment- 4 (retest) with 20% weightage (20 marks).
- 3. In any case, retest will not be considered as an improvement test.

ESSENTIAL READINGS :

Text Books:

- Hayt, W. H, Kemmerly J. E. & Durbin, 'Engineering Circuit Analysis', McGraw Hill Publications, 8th Edition, 2013.
- 2. Charles K. Alexander, Matthew N.O.Sadiku, 'Fundamentals of Electric Circuits', McGraw-Hill Publications, 5th Edition, 2013.

Reference Books:

- Joseph. A. Edminister, 'Electric Circuits Schaum's Outline Series', McGraw-Hill Publications, 6th Edition, 2003.
- Robins & Miller, 'Circuit Analysis Theory and Practice', Delmar Publishers, 5th 2. Edition, 2012.

COURSE EXIT SURVEY

Shall be obtained at the end of the course

COURSE POLICY

ATTENDANCE

- 1. Attendance will be taken by the faculty in all the contact hours. Every student should maintain minimum 75 % physical attendance in these contact hours to attend the end semester examination.
- 2. Any student, who fails to maintain 75% attendance need to appear for the retest. Student who scores more than 50 % marks in the retest will be eligible for attending the end semester examination.
- 3. Students not having 75% minimum attendance at the end of the semester and also fail in retest (scoring less than 50%) will have to RE-DO the course.

ACADEMIC HONESTY & PLAGIARISM

1. Copying in any form during assessments is considered as academic dishonesty and will attract suitable penalty.

FOR APPROVAL

CC-Chairperson