

**DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING**  
**NATIONAL INSTITUTE OF TECHNOLOGY, TIRUCHIRAPPALLI**

COURSE PLAN – PART I								
<b>Name of the programme and specialization</b>	M.Tech Computer Science & Engineering							
<b>Course Title</b>	Design and Analysis of Parallel Algorithms							
<b>Course Code</b>	CS613	<b>No. of Credits</b>	3					
<b>Course Code of Pre-requisite subject(s)</b>	Knowledge of algorithms and complexity.	Basic knowledge of data structures	Basic knowledge of computer architecture					
<b>Session</b>	July 2018	<b>Section (if, applicable)</b>	-					
<b>Name of Faculty</b>	Dr. C. Mala	<b>Department</b>	Department of Computer Science & Engineering					
<b>Email</b>	mala@nitt.edu	<b>Telephone No.</b>	0431- 2503208					
<b>Name of Course Coordinator(s) (if, applicable)</b>	-							
<b>E-mail</b>		<b>Telephone No.</b>						
<b>Course Type</b>	Elective course							
<b>Syllabus (approved in BoS) Do check Pg 18 in the link</b>								
https://www.nitt.edu/home/academics/curriculum/M.Tech-CS-CS-2016.pdf								
<b>COURSE OBJECTIVES</b>								
To learn about parallel computing models, design and analyse parallel algorithms for PRAM machines and Interconnection networks.								
<b>COURSE OUTCOMES (CO)</b>								
To enable the student to design and analyse parallel algorithms.								
<b>Course Outcomes</b>	<b>Aligned Programme Outcomes (PO)</b>							
	<b>PO-1</b>	<b>PO-2</b>	<b>PO-3</b>	<b>PO-4</b>	<b>PO-5</b>	<b>PO-6</b>	<b>PO-7</b>	<b>PO-8</b>
Ability to design parallel algorithms for SIMD machines	S	B	M	S	B	B	M	B
Ability to design parallel algorithms for MIMD machines	S	B	M	S	B	M	M	B
Ability to analyze parallel algorithms for SIMD and MIMD machines	S	M	S	S	M	S	M	B

S = 0.6      M = 0.4      B = 0.0

## COURSE PLAN – PART II

### COURSE OVERVIEW

### COURSE TEACHING AND LEARNING ACTIVITIES

S.No.	Contact Hours	Topic	Mode of Delivery
<b>UNIT - I</b>			
1.	1	Introduction to different models of computation	Pen-Board
2.	2	Array Processors	Pen-Board
3.	2	Multiprocessors	Pen-Board
4.	1	Interconnection networks	Pen-Board
5.	2	Shared memory models control and algorithms	Pen-Board
6.	2	Parallel algorithms for Array processors	Pen-Board
<b>UNIT - II</b>			
7.	1	Broadcast, All sums algorithm	Pen-Board
8.	1	Selection Algorithm	Pen-Board
9.	3	Parallel selection	Pen-Board
10.	3	Searching a random sequence on PRAM models, tree and mesh	Pen-Board
11.	1	Searching a sorted sequence on PRAM models tree and mesh	Pen-Board
<b>UNIT - III</b>			
12.	1	Need for Merging , Merging on PRAM models	Pen-Board
13.	3	Merging on PRAM models	Pen-Board
14.	1	ODD EVEN Merge	Pen-Board
15.	3	Sorting on EREW,CREW and CRCW SIMD models	Pen-Board
16.	1	MIMD Enumeration sort	Pen-Board
<b>UNIT - IV</b>			
17.	3	SIMD algorithms for Matrix operations- Transposition	Pen-Board
18.	2	Matrix by matrix multiplication	Pen-Board
19.	1	Matrix by vector multiplication	Pen-Board
20.	2	Numerical problems- solving systems of linear equations	Pen-Board
21.	1	Finding roots of non linear equations on PRAM models	Pen-Board

**UNIT - V**

22.	3	Graphs algorithms	Pen-Board
23.	2	Finding connected components	Pen-Board
24.	2	Sparse graphs and Dense graphs	Pen-Board
25.	2	Minimum spanning tree	Pen-Board
26.	2	Biconnected components	Pen-Board

**COURSE ASSESSMENT METHODS (shall range from 4 to 6)**

S.No.	Mode of Assessment	Week/Date	Duration	% Weightage
1	Cycle Test 1	As per schedule	1 Hour	20
2	Cycle Test 2	As per schedule	1 Hour	20
3	Assignment			10
CPA	Compensation Assessment*	As per schedule	1 Hour	20
4	Final Assessment *	As per schedule	3 Hours	50

\*mandatory; refer to guidelines on page 4

**COURSE EXIT SURVEY (mention the ways in which the feedback about the course shall be assessed)**

MIS Feedback

**COURSE POLICY (preferred mode of correspondence with students, compensation assessment policy to be specified)**

**MODE OF CORRESPONDENCE (email/ phone etc)**

Email

**COMPENSATION ASSESSMENT POLICY**

One Compensation assessment will be conducted for students who were absent for cycle tests due to genuine reasons.

**ATTENDANCE POLICY (A uniform attendance policy as specified below shall be followed)**

- **At least 75% attendance in each course is mandatory.**
- **A maximum of 10% shall be allowed under On Duty (OD) category.**
- **Students with less than 65% of attendance shall be prevented from writing the final assessment and shall be awarded 'V' grade.**

**ACADEMIC DISHONESTY & PLAGIARISM**

- Possessing a mobile phone, carrying bits of paper, talking to other students, copying from

