

# DEPARTMENT OF CIVIL ENGINEERING

COURSE PLAN – PART I			
Name of the program and specialization	B.Tech. Civil Engineering		
Course Title	Basics of Electrical and Electronics Engineering		
Course Code	EEIR11	No. of Credits	02
Course Code of Pre- requisite subject(s)	Nil		
Session	July-2023	Semester/Section (if, applicable)	I – Sec A
Name of Faculty	Dr. Srinivasulu Jogi	Department	ECE
Official Email	srinivasulu@nitt.edu	Telephone No.	+919491312324
Name of Course Coordinator(s) (if, applicable)			
Official E-mail		Telephone No.	
Course Type (please tick appropriately)	Core course	Elective cou	rse

# Syllabus (approved in BoS)

DC & AC Circuits: Current, voltage, power, Kirchhoff's Laws - circuit elements R, L and C, phasor diagram, impedance, real and reactive power in single phase circuits.

DC & AC Machines: DC Motor, Induction motor, Synchronous motor, Synchronous generator and Transformers- construction, principle of operation, types and applications.

House wiring & safety: Single phase and three phase system – phase, neutral and earth, basic house wring - tools and components, different types of wiring – staircase, florescent lamp and ceiling fan, basic safety measures at home and industry.

Analog Electronics: semiconductor devices – p-n junction diode, Zener diode, BJT, operational amplifier – principle of operation and applications – Introduction to UPS.

Digital Electronics: Introduction to numbers systems, basic Boolean laws, reduction of Boolean expressions and implementation with logic gates.

## Reference:

- Hughes revised by Mckenzie Smith with John Hiley and Keith Brown, Electrical and Electronics Technology, 8th Edition, Pearson, 2012.
- 2. R.J. Smith, R.C. Dorf, Circuits Devices and Systems, 5th Edition, John Wiley and sons, 2001.
- 3. P. S. Dhogal, Basic Electrical Engineering Vol. I & II, 42nd Reprint, McGraw Hill, 2012.

 Malvino, A. P., Leach D. P. and Gowtham Sha, Digital Principles and Applications, 6<sup>th</sup> Edition, Tata McGraw Hill, 2007.

5. Vincent Del Toro, Electrical Engineering Fundamental, Prentice Hall India, 2002.



# COURSE OBJECTIVES

This course aims to equip the students with a basic understanding of DC and AC circuits. The course introduces the students to the core concepts behind DC and AC motors, their principles of operation, and applications. Further, the house wiring and safety measures are comprehensively exposed to the students. Also, the course equips the students with the understanding of basic analog and digital electronics.

MAPPING OF COs with POs	
Course Outcomes	Programme Outcomes (PO) (Enter Numbers only)
The students shall develop and intuitive understanding of basic concepts of DC and AC circuits.	2,3,6
The students shall develop an intuitive understanding of the circuit analysis, basic concepts of electrical machines, house wiring and basics of electronics and be able to apply them in practical situation.	1,2,3,4,6,8,10
Generalize the design techniques and analyze the characteristics of diodes, BJT and all other electronic devices and be able to apply them in practical situation.	5,11
Describe and verify the methods to solve Boolean functions.	11,12

COURSE PLAN - PART II				
COURSE OVERVIEW				
Students	Students get exposure to the fundamentals of electrical devices and circuits. Students will be			
taught about the principles of operation and applications of several electrical machines. Students				
will und	erstand the house wirir	ng and electrical safety techniques and	I have an opportunity to	
make a	practical attempt on ho	use wiring. Further they will be exposed	l to basics of analog and	
digital el	lectronic devices, circuit	s and simple applications.		
COURSE TEACHING AND LEARNING ACTIVITIES (Add more rows)				
S.No.	Week/Contact	Торіс	Mode of Delivery	
	Hours	_	_	
	1 <sup>st</sup> Week	Current, voltage, power, Kirchhoff's	C&T/PPT/Digital	
1	2 contact nours	Laws, Circuit elements R, L and C	Writing pad	
	2 <sup>nd</sup> week	Phasor diagram impedance real and	C&T/DDT/Digital	
2	2 week 2 contact hours	reactive nower in single phase circuits	Writing nad	
	2 001140115		writing pad	



3	3 <sup>rd</sup> week 2 contact hours	Transformers - Construction, principle of operation, types and applications.	C&T/PPT/Digital Writing pad	
4	4 <sup>th</sup> week 2 contact hours	DC Motor - Construction, principle of operation, types and applications.	C&T/PPT/Digital Writing pad	
5	5 <sup>th</sup> week 2 contact hours	nduction motor - Construction, rinciple of operation, types and pplications. <b>C&amp;T/PPT/Digital</b> <b>Writing pad</b>		
6	6 <sup>th</sup> week 2 contact hours	Synchronous motor, Synchronous generator - Construction, principle of operation, types and applications.		
7	7 <sup>th</sup> week 2 contact hours	Single phase and three phase system – phase, neutral and earth, House wiring tools and components, different types of wiring with examples.	C&T/PPT/Digital Writing pad	
		Assessment – 1 (Descriptive-written/Objective/both)	Written/Objective/Both	
8	8 <sup>th</sup> week 2 contact hours	Different types of wiring – staircase, florescent lamp, and ceiling fan, basic safety measures at home and industry.	C&T/PPT/Digital Writing pad	
9	9 <sup>th</sup> week 2 contact hours	Introduction to semiconductor and semiconductor devices and types. PN junction diode operation. Characteristics of diode and application.	C&T/PPT/Digital Writing pad	
10	10 <sup>th</sup> week 2 contact hours	Zener diode - Characteristics, Introduction to BJT, different configuration of BJT and application.	C&T/PPT/Digital Writing pad	
11	11 <sup>th</sup> week 2 contact hours	Operational amplifier - Principle of operation and applications, Introduction to UPS.	C&T/PPT/Digital Writing pad	
		Assessment – 2 (Descriptive-written/Objective/both)	Written/Objective/Both	
12	12 <sup>th</sup> week 2 contact hours	Introduction to numbers systems, basic Boolean laws <b>C&amp;T/PPT/Digital</b> Writing pad		



SHIRAY						
13	13 <sup>th</sup> week 2 contact hours	<b>13<sup>th</sup> week</b> <b>2 contact hours</b> Reduction of Boolean expressions and implementation with logic gates.			C&T/PPT/Digital Writing pad	
14	End Semester Assessment (Descriptive-written/Objective/both)			Written/Objective/Both		
	C&T – Chalk and Talk, PPT – Power point					
COUR	SE ASSESSMENT MET	HODS (s	hall range from 4 to	6 <b>)</b>		
S.No.	Mode of Assessm	ent	Week/Date	Durati	on	% Weightage
1	Assessment – 1 (Descriptive-written/Ob both-Modules 1&	- jective/ 2)	As per academic calendar	60 mins		20%
2	Assessment – 2 (Descriptive-written/Ob both-Modules 3&	2 ojective/ 4)	As per academic calendar	60 mir	าร	20%
3	Assessment – 3 Assignments/Quiz – <b>0 mode/submissic</b> (Institute Procedure-Mod	3 Online On dules1-4)	Will be announced in the class			10%
СРА	Compensation Asses (Modules 1-4)	ssment* )	As per academic calendar	60 mir	าร	
4	Final Assessmen (Descriptive-written/Ob both-Modules 1-5	it* ijective/ 5)	As per academic calendar	180 mins 50%		50%
*mandatory; refer to guidelines on pages 4, 5, and 6						
COUR	SE EXIT SURVEY (ment	tion the w	avs in which the fee	dback abo	ut the	course shall be

#### assessed)

Feedback from the students through MIS and class committee meetings.

**COURSE POLICY (**including compensation assessment to be specified)

## MODE OF CORRESPONDENCE (email/phone etc)

All the students are advised to check their NITT WEBMAIL/MS Teams regularly. All the correspondence (schedule of classes/ schedule of assessment/ course material/ any other information regarding this course) will be intimated in Class/MS Teams only.

## ASSESSMENT POLICY

- 1. Attending all the assessments is MANDATORY for every student.
- If any student is not able to attend any of the Continuous Assessments due to genuine reason, student is permitted to attend the compensation assessment\* (CPA) with Corresponding weightage. (This is not valid for students who have attendance lag also.)
- 3. Please refer institute B.Tech Regulations/guidelines for grading policy.



ATTENDANCE POLICY (A uniform attendance policy as specified below shall be followed)		
<ul> <li>At least 75% attendance in each course is mandatory.</li> </ul>		
<ul> <li>A maximum of 10% shall be allowed under On Duty (OD) category.</li> </ul>		
<ul> <li>Students with less than 65% of attendance shall be prevented from writing the final assessment and shall be awarded 'V' grade.</li> </ul>		
ACADEMIC DISHONESTY & PLAGIARISM		
<ul> <li>Possessing a mobile phone, carrying bits of paper, talking to other students, copying from others during an assessment will be treated as punishable dishonesty.</li> </ul>		
<ul> <li>Zero mark to be awarded for the offenders. For copying from another student, both students get the same penalty of zero mark.</li> </ul>		
• The departmental disciplinary committee including the course faculty member, PAC chairperson and the HoD, as members shall verify the facts of the malpractice and award the punishment if the student is found guilty. The report shall be submitted to the Academic office.		
<ul> <li>The above policy against academic dishonesty shall be applicable for all the programmes.</li> </ul>		
ADDITIONAL INFORMATION, IF ANY		
Queries may also be emailed to the course faculty directly at srinivasulu@nitt.edu.		
FOR APPROVAL		
NR TIDIS STATION		
Course Faculty The CC- Chairperson 1 March HOD HOD		